

ABSTRACT OF THE DISCLOSURE

An electronic circuit including a plurality of D/A converters is operated to provide uniform outputs from the D/A converters even if the D/A converters have different levels of offset values by providing each D/A converter with a memory for offset correction digital data and an adder for adding the offset correction digital data to a digital input signal to the D/A converter. The uniformized outputs from the A/D converters may be used for providing a uniform display on a liquid crystal display apparatus. The liquid crystal apparatus may be provided with a pair of common signal lines for separately supplying positive polarity-picture signals and negative-polarity picture signals to an active matrix substrate for driving the liquid crystal.

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